	Application No.	Applicant(s)
Notice of Allowability	10/655,712	BRONFER ET AL.
	Examiner	Art Unit
	Tuan T. Lam	2816
The MAILING DATE of this c mmunicati n app ars on th c ver sh et with the correspondenc address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to the paper filed 9/23/2003.		
2. X The allowed claim(s) is/are <u>1-18</u> .		
3. X The drawings filed on 9/4/2003 are accepted by the Examiner.		
 4.		
 Attachm nt(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. Interview Summary (Paper No./Mail Date 8), 7. Examiner's Amendm	è

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Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance: the prior art of record fails to teach or fairly suggest generating a first mask signal so as to mask said first clock until a next positive transition of said second clock, generating a second mask signal having a second time duration of one second clock period less said first time duration to mask said second clock so as to compensate for said ambiguity of said first clock cycle of said second as called for in claim 1, a counter loaded initially with a value corresponding to a period of said slow clock, enabling count down of said counter while said mask signal is active and freezing said counter thereafter, generating a second mask signal until said counter reaches zero as called for in claim 5, measuring the ambiguity of a firs slow clock cycle, compensating the last cycle of said slow clock by an amount corresponding to said previously measured ambiguity as called for in claim 9, a counter to count down a period of said slow clock, a timing control circuit and the associated functions as recited in lines 7-16. Therefore, claims 1-18 are presently allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam

Primary Examiner Art Unit 2816

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